



*Fee Only*

501.42869X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: H. CHAKIHARA, et al.  
Application No.: 10/629,733  
Filed: July 30, 2003  
For: A SEMICONDUCTOR EMORY DEVICE AND A METHOD OF  
MANUFACTURING THE SAME, A METHOD OF  
MANUFACTURING A VERTICAL MISFET AND A VERTICAL  
MISFET, AND A METHOD OF MANUFACTURING A  
SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR  
DEVICE  
Art Unit: 2826  
Examiner: F. Abraham

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §§ 1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 26, 2004

Sir:

In the matter of the above-identified application, Applicants are submitting herewith a copy of a U.S. application Serial No. 10/465,550 and copies of the documents listed in the attached form PTO/SB/08A, cited in the related application for the Examiner's consideration.

This Information Disclosure Statement is being submitted, on information and belief, before the mailing date of a first Office Action on the merits.

To the extent the documents listed on the attached form PTO/SB/08A are not in the English language, the requirement of 37 CFR § 1.98(a)(3). For a concise explanation of the relevance is satisfied by an English language version abstract or